

Hardware/Software Co-Design for Data Flow Dominated Embedded Systems

Ralf Niemann

Foreword by Peter Marwedel

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Hardware Software Co Design For Data Flow Dominated Embedded Systems

Sociedade Brasileira de Computação



Hardware Software Co Design For Data Flow Dominated Embedded Systems:

Hardware/Software Co-Design for Data Flow Dominated Embedded Systems Ralf Niemann,1998-10-31 Introduces different tasks of hardware software co design including system specification hardware software partitioning co synthesis and co simulation Summarizes and classifies co design tools and methods for these tasks and presents the co design tool COOL useful for solving co design tasks for the class of data flow dominated embedded systems Primary emphasis is on hardware software partitioning and the co synthesis phase and their coupling A mathematical formulation of the hardware software partitioning problem is given and several novel approaches are presented and compared for solving the partitioning problem Annotation copyrighted by Book News Inc Portland OR *Hardware/Software Co-Design for Data Flow Dominated Embedded Systems* Ralf Niemann,1998-11-14 Many of the modern applications of microelectronics require huge amounts of computations Despite all recent improvements in fabrication technologies some of these computations have to be performed in hardware in order to meet deadlines However controlling computations by software is frequently preferred due to the larger flexibility Hence in general modern applications require a mix of software based and hardware based computations Applications using this mix can be designed with the help of hardware software co design systems Many such co design systems have been described so far references can be found in this book but many of these are based on heuristics In this book Niemann describes a co design system which is based on sound modeling techniques This system has the following salient features Precise cost and performance figures Design decisions for implementing a certain function in hardware or software are based on cost and performance figures for the different design alternatives Hence good designs can only be expected if these figures are accurate In order to achieve excellent accuracy Niemann takes a new approach the cost of software implementations is derived from the data available about the target processors and from knowledge about the code size the performance of software implementations is computed by compiling the given function and then using static analysis for computing worst case execution times the cost of hardware implementation is estimated by running higher level synthesis tools the performance of hardware implementations is again computed by using static analysis *Hardware / Software Co-Design For Data Flow Dominated Embedded Systems* Niemann,2008-02-01 System Level

Hardware/Software Co-Design Joris van den Hurk,Jochen A.G. Jess,2013-04-17 Hierarchical design methods were originally introduced for the design of digital ICs and they appeared to provide for significant advances in design productivity Time to Market and first time right design These concepts have gained increasing importance in the semiconductor industry in recent years In the course of time the supportive quality of hierarchical methods and their advantages were confirmed System Level Hardware Software Co design An Industrial Approach demonstrates the applicability of hierarchical methods to hardware software codesign and mixed analogue digital design following a similar approach Hierarchical design methods provide for high levels of design support both in a qualitative and a quantitative sense In the qualitative sense the presented methods

support all phases in the product life cycle of electronic products ranging from requirements analysis to application support. Hierarchical methods furthermore allow for efficient digital hardware design, hardware software codesign, and mixed analogue/digital design on the basis of commercially available formalisms and design tools. In the quantitative sense, hierarchical methods have prompted a substantial increase in design productivity. System Level Hardware Software Co design: An Industrial Approach reports on a six year study during which time the number of square millimeters of normalized complexity an individual designer contributed every week rose by more than a factor of five. Hierarchical methods therefore enabled designers to keep track of the ever increasing design complexity while effectively reducing the number of design iterations in the form of redesigns. System Level Hardware Software Co design: An Industrial Approach is the first book to provide a comprehensive coherent system design methodology that has been proven to increase productivity in industrial practice. The book will be of interest to all managers, designers, and researchers working in the semiconductor industry.

Hardware/Software Co-Design Jørgen Staunstrup, Wayne Wolf, 2013-04-17. Introduction to Hardware Software Co Design presents a number of issues of fundamental importance for the design of integrated hardware software products such as embedded communication and multimedia systems. This book is a comprehensive introduction to the fundamentals of hardware software co design. Co design is still a new field but one which has substantially matured over the past few years. This book, written by leading international experts, covers all the major topics including fundamental issues in co design, hardware software co synthesis, algorithms, prototyping, and emulation, target architectures, compiler techniques, specification, and verification, system level specification. Special chapters describe in detail several leading edge co design systems including Cosyma, LYCOS, and Cosmos. Introduction to Hardware Software Co Design contains sufficient material for use by teachers and students in an advanced course of hardware software co design. It also contains extensive explanation of the fundamental concepts of the subject and the necessary background to bring practitioners up to date on this increasingly important topic.

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002. This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s.

The Codesign of Embedded Systems: A Unified Hardware/Software Representation Sanjaya Kumar, James H. Aylor, Barry W. Johnson, Wm.A. Wulf, 1995-11-30. Current practice dictates the separation of the hardware and software development paths early in the design cycle. These paths remain independent with very little interaction occurring between them until system integration. In particular, hardware is often specified without fully appreciating the computational requirements of the software. Also, software development does not influence hardware development and does not track changes made during the hardware design phase. Thus, the ability to explore hardware software tradeoffs is restricted, such as the movement of functionality from the software domain to the hardware domain and vice versa, or the modification of the hardware software interface. As a result, problems that are encountered during system integration may

require modification of the software and or hardware resulting in potentially significant cost increases and schedule overruns To address the problems described above a cooperative design approach one that utilizes a unified view of hardware and software is described This approach is called hardware software codesign The Codesign of Embedded Systems develops several fundamental hardware software codesign concepts and a methodology that supports them A unified representation referred to as a decomposition graph is presented which can be used to describe hardware or software using either functional abstractions or data abstractions Using a unified representation based on functional abstractions an abstract hardware software model has been implemented in a common simulation environment called ADEPT Advanced Design Environment Prototyping Tool This model permits early hardware software evaluation and tradeoff exploration Techniques have been developed which support the identification of software bottlenecks and the evaluation of design alternatives with respect to multiple metrics The application of the model is demonstrated on several examples A unified representation based on data abstractions is also explored This work leads to investigations regarding the application of object oriented techniques to hardware design The Codesign of Embedded Systems A Unified Hardware Software Representation describes a novel approach to a topic of immense importance to CAD researchers and designers alike Hardware/Software Co-Design Giovanni DeMicheli,M.G. Sami,2013-11-11 Concurrent design or co design of hardware and software is extremely important for meeting design goals such as high performance that are the key to commercial competitiveness Hardware Software Co Design covers many aspects of the subject including methods and examples for designing 1 general purpose and embedded computing systems based on instruction set processors 2 telecommunication systems using general purpose digital signal processors as well as application specific instruction set processors 3 embedded control systems and applications to automotive electronics The book also surveys the areas of emulation and prototyping systems with field programmable gate array technologies hardware software synthesis and verification and industrial design trends Most contributions emphasize the design methodology the requirements and state of the art of computer aided co design tools together with current design examples **Hardware-software Co-design for Embedded Systems** Anuradha Mulukutla,1998 **Proceedings of the ... International Symposium on Hardware/Software Codesign** ,2002 14th Symposium on Integrated Circuits and Systems Design Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BDD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book

News Inc A Practical Introduction to Hardware/Software Codesign Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction to the subject of embedded systems design with emphasis on integration of custom hardware components with software The key problem addressed in the book is the following how can an embedded systems designer strike a balance between flexibility and efficiency The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem The book covers four topics in hardware software codesign fundamentals the design space of custom architectures the hardware software interface and application examples The book comes with an associated design environment that helps the reader to perform experiments in hardware software codesign Each chapter also includes exercises and further reading suggestions Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera which will make the material in this book applicable to a greater number of courses where these tools are already in use More examples and exercises have been added throughout the book If I were teaching a course on this subject I would use this as a resource and text If I were a student who wanted to learn codesign I would look for a course that at least used a similar approach If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective I would read this book first before any other When I first started learning about codesign as a practitioner a book like this would have been the perfect introduction Grant Martin Tensilica *Fourth International Workshop on Hardware/Software Co-Design, Codes/CASHE '96* Donald E. Thomas, Rolf Ernst, 1996 Embedded architecture co synthesis and system integration B Lin S Vercauteren and H De Man A multi level transformation approach to HW SW codesign a case study T K Y Cheung G Hellestrand and P Kanthamanon Fully parallel hardware software codesign for multi dimensional DSP applications M Sheliga N L Passos and E H M Sha A co design methodology based on formal specification and high level estimation C Carreras and others Speed up estimation for HW SW systems W Hardt and W Rosenstiel A framework for interactive analysis of timing constraints in embedded systems R K Gupta The interplay of run time estimation and granularity in HW SW partitioning J Henkel and R Ernst Partitioning and exploration strategies in the TOSCA co design flow A Balboni W Fornaciari and D Sciuto Process partitioning for distributed embedded systems J Hou and W Wolf Two level partitioning of image processing algorithms for the parallel map oriented machine R W Hartenstein J Becker and R Kress PACE a dynamic programming algorithm for hardware software partitioning P V Knudsen and J Madsen A model for the coanalysis of hardware and software architectures F Rose and others A case study in co design of communication controllers R Gerndt Formal verification of embedded systems based on CFM networks F Balarin and others Towards a model for hardware and software functional partitioning F Vahid and T dm Le Implications of codesign as a natural constituent of a systems engineering discipline for computer based systems M Voss and O Hammerschmidt Uninterpreted co simulation for performance evaluation of HW SW systems J P Calvez D Heller and O Pasquier Fast and accurate hardware software co simulation using software timing estimates C Passerone and others

International Symposium on System Synthesis ,2002 Intelligent Systems and Automation Hichem Arioui,Rochdi Merzouki,Hadj Ahmed Abbassi,2008-06-17 All papers have been peer reviewed The Intelligent Systems and Automation conference will be organized for its first edition between June 30th and July 02nd 2008 where it will be held at Annaba in Algeria Africa CISA encourages the diverse research actors and the industrial one to present the last headways in Robotics and Automation fields notably the experimental demonstration of prototypes CISA tries to give to the unsupported researchers a significant access to the new technologies and theories around the topics of Robotics Automation The organizers want to give the necessary scientific documents to disposal of the PhD students and researchers of the Mediterranean region CISA wants to offer to the youth researchers from the south Mediterranean region the opportunities to exchange and to discuss their scientific contributions with the other researchers from all over the world **Proceedings IECON.** ,2002 Memorandum ,2000 System-on-a-chip Rochit Rajsuman,2000 Starting with a basic overview of system on a chip SoC including definitions of related terms this new book helps you understand SoC design challenges and the latest design and test methodologies You see how ASIC technology evolved to an embedded cores based concept that includes pre designed reusable Intellectual Property IP cores that act as microprocessors data storage devices DSP bus control and interfaces all stitched together by a User s Defined Logic UDL *Proceedings of the Fifth International Workshop on Hardware/Software Co-Design (Codes/CASHE '97)* ,1997 Contains papers from the March 1997 workshop in sections on scheduling and allocation target architectures and debugging optimization communication issues synthesis of run time environments modeling and simulation acceleration and trading off hardware and software Topics include interface optimization during hardware software partitioning software architecture synthesis for retargetable real time embedded systems software acceleration using coprocessors and an evolutionary approach to system level synthesis No index Annotation copyrighted by Book News Inc Portland OR **Proceedings of the Seventh International Workshop on Hardware/Software Codesign (CODES'99)** Association for Computing Machinery,1999

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