



**COMPUTER-AIDED DESIGN  
AND DIAGNOSIS METHODS  
FOR BIOMEDICAL  
APPLICATIONS**

Edited by  
Manan Bajaj and G.R. Srinivas

 CRC Press

# Formal Methods In Computeraided Design

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## **Formal Methods In Computeraided Design:**

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**Formal Methods in Computer-Aided Design** Warren A. Jr. Hunt, Steven D. Johnson, 2007-11-29 The biannual Formal Methods in Computer Aided Design conference FMCAD 2000 is the third in a series of conferences under that title devoted to the use of discrete mathematical methods for the analysis of computer hardware and software The work reported in this book describes the use of modeling languages and their associated automated analysis tools to specify and verify computing systems Functional verification has become one of the principal costs in a modern computer design effort In addition verification of circuit models timing power etc requires even more effort FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using discrete mathematical modeling and verification It is noted with interest by the conference chairmen how this area has grown from just a few people 15 years ago to a vibrant area of research development and deployment It is clear that these methods are helping reduce the cost of designing computing systems As an example of this potential cost reduction we have invited David Russino of Advanced Micro Devices Inc to describe his verification of floating point algorithms being used in AMD microprocessors The program includes 30 regular presentations selected from 63 submitted papers

**Formal Methods in Computer-Aided Design** Mark D. Aagaard, John W. O'Leary, 2003-06-30 This volume contains the proceedings of the Fourth Biennial Conference on Formal Methods in Computer Aided Design FMCAD The conference is devoted to the use of mathematical methods for the analysis of digital hardware circuits and systems The work reported in this book describes the use of formal mathematics and associated tools to design and verify digital hardware systems Functional verification has become one of the principal costs in a modern computer design effort FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using discrete mathematical modeling and verification Over the past 20 years this area has grown from just a few academic researchers to a vibrant worldwide community of people from both academia and industry This volume includes 23 papers selected from the 47 submitted papers each of which was reviewed by at least three program committee members The history of FMCAD dates back to 1984 when the earliest meetings on this topic occurred as part of IFIP WG10.2

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**Formal Methods in Computer-Aided Design** Alan J. Hu, Andrew K. Martin, 2005-01-18 These are the proceedings of the fifth international conference Formal Methods in Computer Aided Design FMCAD held 15-17 November 2004 in Austin Texas USA The conference provides a forum for presenting state of the art tools methods algorithms and theory for the application of formalized reasoning to all aspects of computer aided system design including specification verification synthesis and testing FMCAD's heritage dates back 20 years to some of the earliest conferences on the subject of formal reasoning and computer aided design Since 1996 FMCAD has assumed its present form held biennially in North America alternating with its sister conference CHARME in Europe We are delighted to report that our research community continues to flourish we received 69 paper submissions with many more high quality papers than we had room to accept After a rigorous review process in which each paper received at least three and typically four or more independent reviews we accepted 29 papers for the conference and inclusion in this volume The conference also included invited talks from Greg Spirakis of Intel Corporation and Wayne Wolf of Princeton University A conference of this size requires the contributions of numerous people On the technical side we are grateful to the program committee and the additional reviewers for their countless hours reviewing submissions and ensuring the intellectual quality of the conference We would also like to thank the steering committee for their wisdom and guidance On the logistical side we thank Christa Mace for designing our website and attending to countless organizational tasks And we thank our corporate sponsors AMD IBM Intel and Synopsys for financial support that helped make this conference possible

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